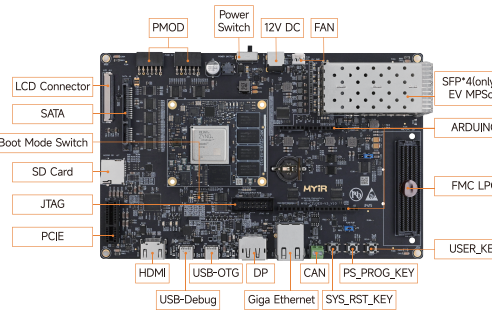




MYD-CZU3EG-V3

快速使用指南

1. 硬件接口及注意事项



MYD-CZU3EG-V3开发板正面接口图

注意事项:

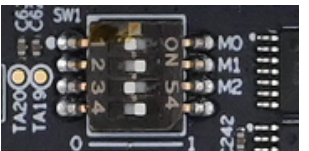
1. 板卡出厂时已经烧入了出厂镜像程序在核心板的 QSPI 中,您只需要按下面步骤就可以开机启动 (请确保板卡设置在 QSPI 启动模式)。
2. 使用 12V/1.5A 电源,以避免损坏板卡。

2. 快速操作指引

第一步: 设置BOOT选择

开发板提供四种启动方式供选择,分别是 JTAG, TF Card, eMMC 和 Quad-SPI 启动,可通过拨码开关设置为 QSPI 启动模式。

Mode	SW1	M0	M1	M2
JTAG		ON	ON	ON
QSPI		ON	OFF	ON
TF		OFF	ON	OFF
eMMC		ON	OFF	OFF



说明:
图中为 TF Card 启动方式
其他启动方式按表所示配置

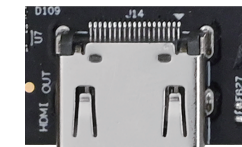
第二步: 连接调试串口线

用 TYPE-C USB 线缆连接到底板 USB 接口 (J15), 此接口是 USB C 转 USB2.0 接口



第三步: 连接HDMI显示器

使用 HDMI 线缆连接板子 J14 和 HDMI 显示器, 如无需显示,也可以不连接。



第四步: 连接电源线

使用标配 12V 电源供电,插入开发板电源接口 J2。



第五步: 开启电源开关, 板卡上电

开启电源开关 J3, 板卡上电。



第六步: 打开调试串口, 配置参数, 开始调试

在主机打开串口终端软件,配置如下参数:

波特率	数据位	停止位	校验位	其他
115200	8 位	1 位	无	无流控

用户名: root

启动完成后串口终端输出信息如下,root 用户自动登录。

```
*****
The PetaLinux source code and images provided/generated are for demonstration purposes only.
Please refer to https://xilinx-wiki.atlassian.net/wiki/spaces/A/pages/2741928025/Moving+from+PetaLinux+
to+Production+Deployment
for more details.
*****
PetaLinux 2024.2+release-S11061705 MYD-CZU3EG ttyPS0
MYD-CZU3EG login: root (automatic login)
root@MYD-CZU3EG:~#
```

产品资料链接

dev.myir.cn

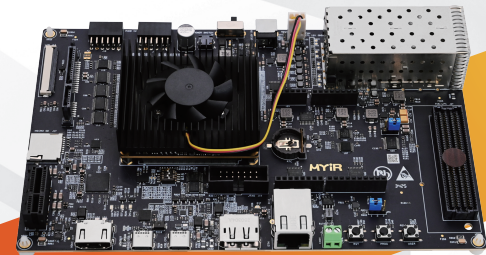
技术支持

如您遇到使用问题,欢迎与我们取得联系。

邮箱: support.cn@myir.cn

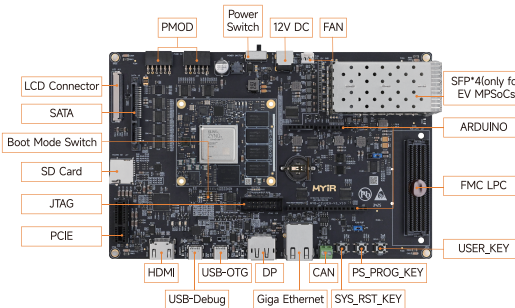
电话: 0755-22316235

网址: www.myir.cn



MYD-CZU3EG-V3 Quick Start Guide

1. Hardware Interfaces and Cautions



MYD-CZU3EG-V3 Top-view

Cautions:

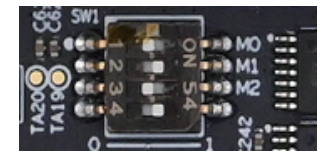
1. The factory image program has been pre-flashed into the QSPI of the core board before shipment. You only need to follow the steps below to power on the device (please ensure the board is set to QSPI boot mode).
2. Use a 12V/1.5A power supply to avoid damaging the board.

2. Operating instructions

Step1: BOOT Mode Setting

The development board provides four boot modes for choice, namely JTAG, TF Card, eMMC and QSPI boot mode, which can be set to QSPI boot mode by dip switch.

Mode	SW1	M0	M1	M2
JTAG		ON	ON	ON
QSPI		ON	OFF	ON
TF		OFF	ON	OFF
eMMC		ON	OFF	OFF

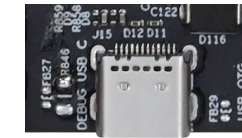


Note:

The diagram shows the configuration for TF Card boot mode.

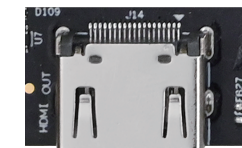
Step 2: Connect the debugging serial cable

Connect the TYPE-C USB cable to the mainboard USB port (J15), which is the USB C to USB2.0 interface.



Step 3: Connect the HDMI monitor

Use an HDMI cable to connect the board J14 to the HDMI display. If no display is required, do not connect it.



Step 4: Connect the power cable

Use the standard 12V power supply and plug it into the development board power port J2.



Step 5: Power on the board

Turn on the power switch J3 to power on the carrier board.



Step 6: Open the debugging serial port and set parameters

Open the serial port software on the host and set the following parameters:

Baud rate	Data bits	Stop bit	Parity	Other
115200	8	1	None	No Flow control

User:root

The serial terminal output information is as follows.

```
*****
The PetaLinux source code and images provided/generated are for demonstration purposes only.
Please refer to https://xilinx-wiki.atlassian.net/wiki/spaces/A/pages/2741928025/Moving+from+PetaLinux+
to+Production+Deployment
for more details.
*****
PetaLinux 2024.2+release-S11061705 MYD-CZU3EG ttyPS0
MYD-CZU3EG login: root (automatic login)
root@MYD-CZU3EG:~#
```

Product Information Link

developer.myir.cn

Technical Support

Please do not hesitate to contact us for technical support:

Email: support@myir.cn

Telephone: +86-755-22984836

Website: en.myir.cn