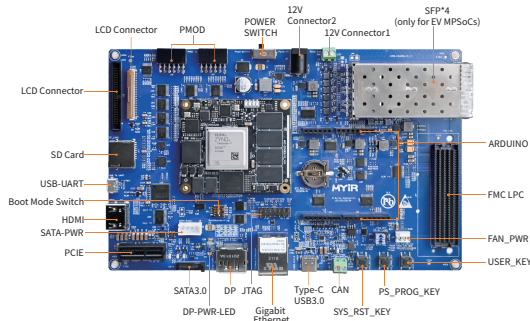


MYD-CZU3EG/4EV/5EV-V2

快速使用指南

1. 硬件接口及注意事项

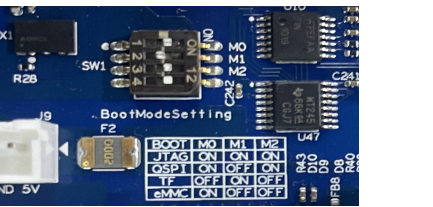


- 注意事项:
1. 板卡出厂时已经烧入了出厂镜像程序在核心板的 eMMC 与 QSPI 中,您只需要按下面步骤就可开机启动(请确保核心板启动模式设置为 QSPI)。
 2. 建议使用板卡出厂配套的电源适配器(12V),以避免损坏板卡。

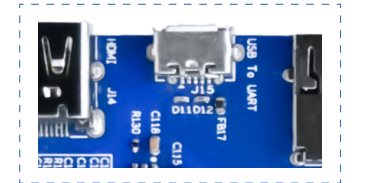
2. 快速操作指引

第一步:设置BOOT选择
开发板提供四种启动方式供选择,分别是 JTAG,SD1,eMMC 和 Quad-SPI 启动,可通过拨码开关设置为 QSPI 启动模式。

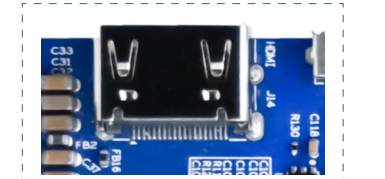
Mode	SW1	M0	M1	M2
JTAG		ON	ON	ON
QSPI		ON	OFF	ON
TF		OFF	ON	OFF
eMMC		ON	OFF	OFF



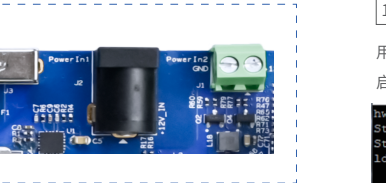
第二步:连接调试串口线
用 USB A-Micro USB 线缆连接到底板 USB 接口 (J15),此接口是 UART 转 USB2.0 接口



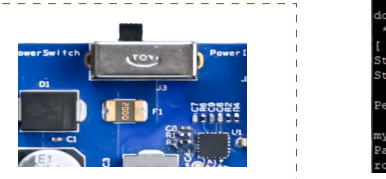
第三步:连接HDMI显示器
使用 HDMI 线缆连接板子 J14 和 HDMI 显示器,如无需显示,也可以不连接。



第四步:连接电源线
使用标配 12V 电源供电,插入开发板电源接口 J2。



第五步:开启电源开关,板卡上电
开启电源开关(SW5),板卡上电。



第六步:打开调试串口,配置参数,开始调试
在主机打开串口终端软件,配置如下参数:

波特率	数据位	停止位	极性	其他
115200	8 位	1 位	无	无流控

用户名: root, 密码: root
启动完成后串口终端输出信息如下,root 用户自动登录。

```
hwclock: settimeofday() failed: Invalid argument
Starting internet superserver: inetd.
Starting syslogd/klogd: haveged: haveged: ver: 1.9.5; arch: generic; vend: ; bui
ld: (gcc 9.2.0 CTV); collect: 128K

haveged: haveged: cpu: (VC); data: 16K (D); inst: 16K (D); idx: 11/40; sz: 15456
/64452

haveged: haveged: tot tests(BA8): A:1/1 B:1/1 continuous tests(B): last entropy
estimate 8.00239

haveged: haveged: fills: 0, generated: 0

done
* Starting Avahi mDNS/DNS-SD Daemon: avahi-daemon
[ 27.588618] random: crng init done [ ok ]
Starting Telephony daemon
Starting tcf-agent: OK

PetaLinux 2020.1 myd_zu3eg4ev_2020 ttyPS0

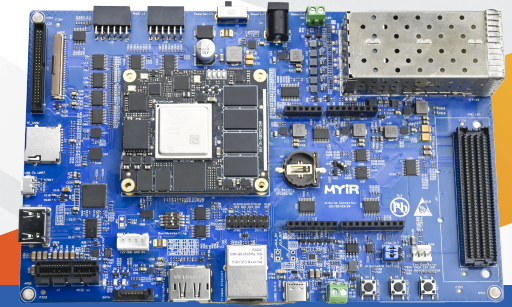
myd_zu3eg4ev_2020 login: root
Password:
root@myd_zu3eg4ev_2020:~#
```

产品资料链接

ZU3EG: <http://down.myr-tech.com/MYD-CZU3EG/>
 ZU4EV/5EV: http://down.myr-tech.com/MYD-CZU4EV_5EV/

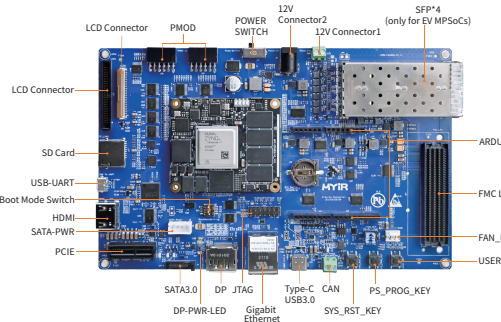
技术支持

如您遇到使用问题,欢迎与我们取得联系。
 邮箱: support.cn@myirtech.com
 电话: 027 5962 1648
 网址: www.myr-tech.com



MYD-CZU3EG/4EV/5EV-V2 Development Kit Quick Start Guide

1. Hardware Interface



Top View of MYD-CZU3EG-V2 Development Kit

Notes:

1. The board has already burned the factory image program into its eMMC and QSPI before delivery. You only need to follow the following steps to boot the board (please make sure the board is set to QSPI boot mode).
2. To avoid damaging the board, please make sure using the power adapter (12V DC) coming with the board in the box.

2. Quick steps to hands-on

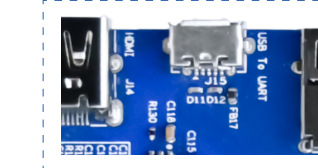
Step1: Configure the boot options

The development board provides four boot modes for choice, namely JTAG, SD1, eMMC and Quad-SPI boot mode, which can be set to QSPI boot mode by dip switch.

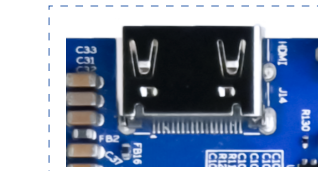
Mode	SW1	M0	M1	M2
JTAG		ON	ON	ON
QSPI		ON	OFF	ON
TF		OFF	ON	OFF
eMMC		ON	OFF	OFF



Step 2: Connect the debugging serial cable
Connect the USB A-Micro USB cable to the mainboard USB port (J15), which is the UART to USB2.0 interface.



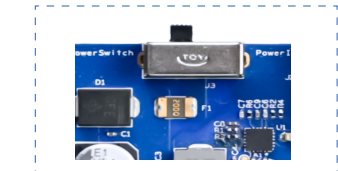
Step 3: Connect the HDMI monitor
Use an HDMI cable to connect the board J14 to the HDMI display. If no display is required, do not connect it.



Step 4: Connect the power cable
Use the standard 12V power supply and plug it into the development board power port J2.



Step 5: Power on the core board
Turn on the power switch J3 to power on the carrier board.



Step 6: Open the debugging serial port and set parameters
Open the serial port software on the host and set the following parameters:

Band rate	Data bits	Stop bit	Parity	Other
115200	8	1	No	No Flow control

User:root ,Passwd: root
The serial terminal output information is as follows.

```

hwclock: settimeofday() failed: Invalid argument
Starting internet superserver: inetd.
Starting syslogd/klogd: haveged: haveged: ver: 1.9.5; arch: generic; vend: ; bui
ld: (gcc 9.2.0 CTV); collect: 128K

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done
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PetaLinux 2020.1 myd_zu3eg4ev_2020 ttyPS0
myd_zu3eg4ev_2020 login: root
Password:
root@myd_zu3eg4ev_2020:~#
    
```

Download Software and Documents

ZU3EG: <http://d.myirtech.com/MYD-CZU3EG/>
ZU4EV/5EV: http://d.myirtech.com/MYD-CZU4EV_5EV/

Technical Support

Please do not hesitate to contact us for technical support:
Email: support@myirtech.com
Telephone: (+86) 0755 22984836
Website: www.myirtech.com