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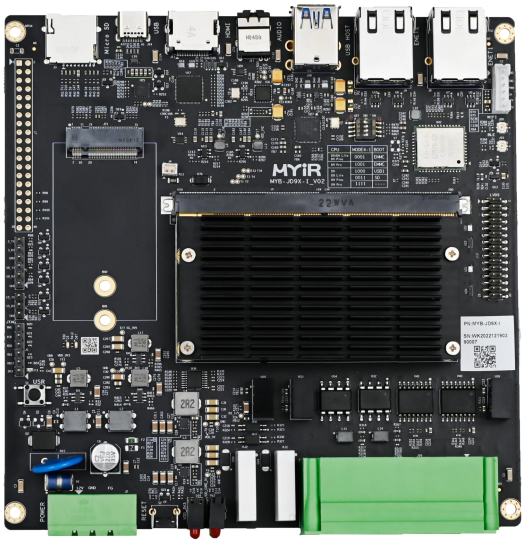
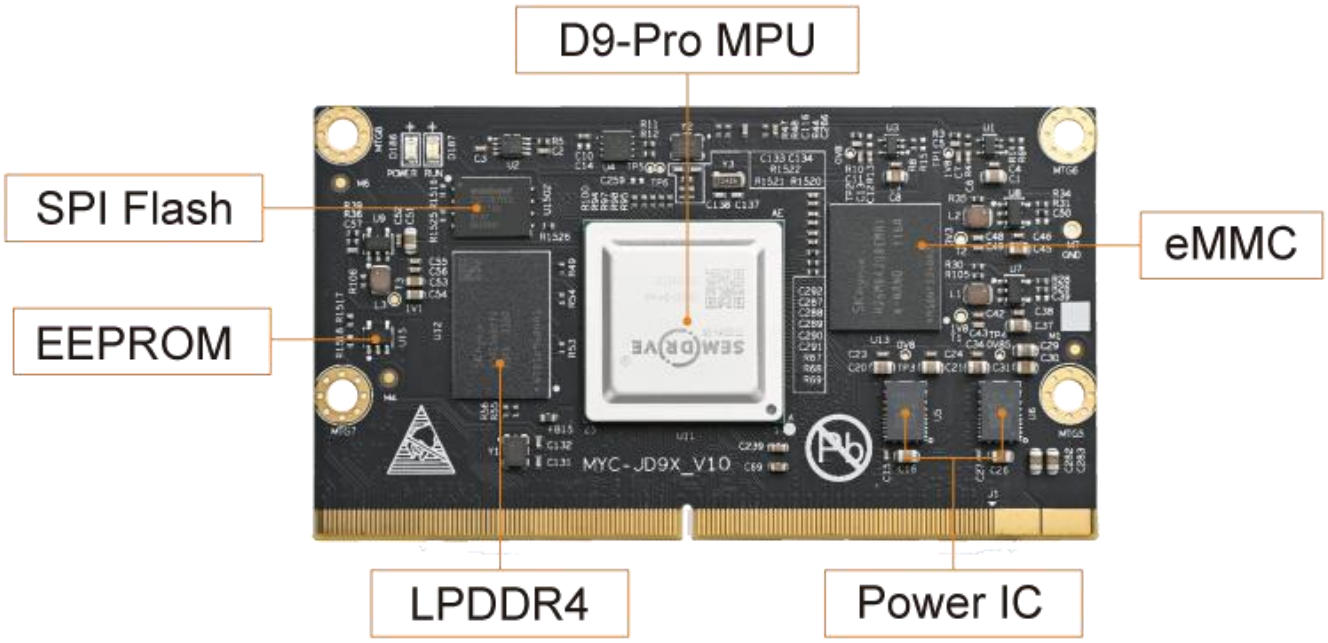
MYD-JD9360 Development Board Overview



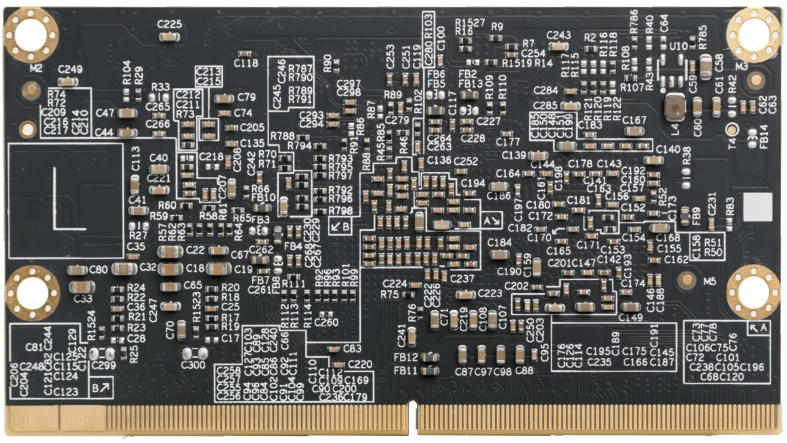
- ✓ *MYC-JD9360 SOM as Controller Board*
- ✓ *SemiDrive D9-Pro High Performance Processor based on 1.6GHz Six ARM Cortex-A55 and Cortex-R5 Cores*
- ✓ *2GB LPDDR4, 16GB eMMC Flash, 16MB SPI Nor Flash, 256Kbit EEPROM*
- ✓ *2 x USB 3.0 Host, 1 x USB 2.0 DRD, 2 x CAN, 2 x RS485, 2 x RS232, Micro SD card Slot*
- ✓ *Supports 2 x Gigabit Ethernet, WiFi/Bluetooth and 4G/5G LTE*
- ✓ *LVDS, HDMI, Camera Interface (MIPI-CSI), Audio Input/Output*
- ✓ *Supports for Linux OS*

The **MYD-JD9360 Development Board** provides a complete evaluation platform for SemiDrive D9-Pro Processor which features six-core Arm Cortex-A55 and one real-time Cortex-R5 co-processor, with an integrated Neural Processing Unit (NPU) operating at up to 0.8 TOPS, 100GFLOPS PowerVR 9XM GPU and a pretty fast VPU to focus on high-performance display and control all-in-one machines, typical applications are industrial robots, engineering machinery T-BOX, intelligent cockpits, in-vehicle entertainment, intelligent medical equipment and so on.

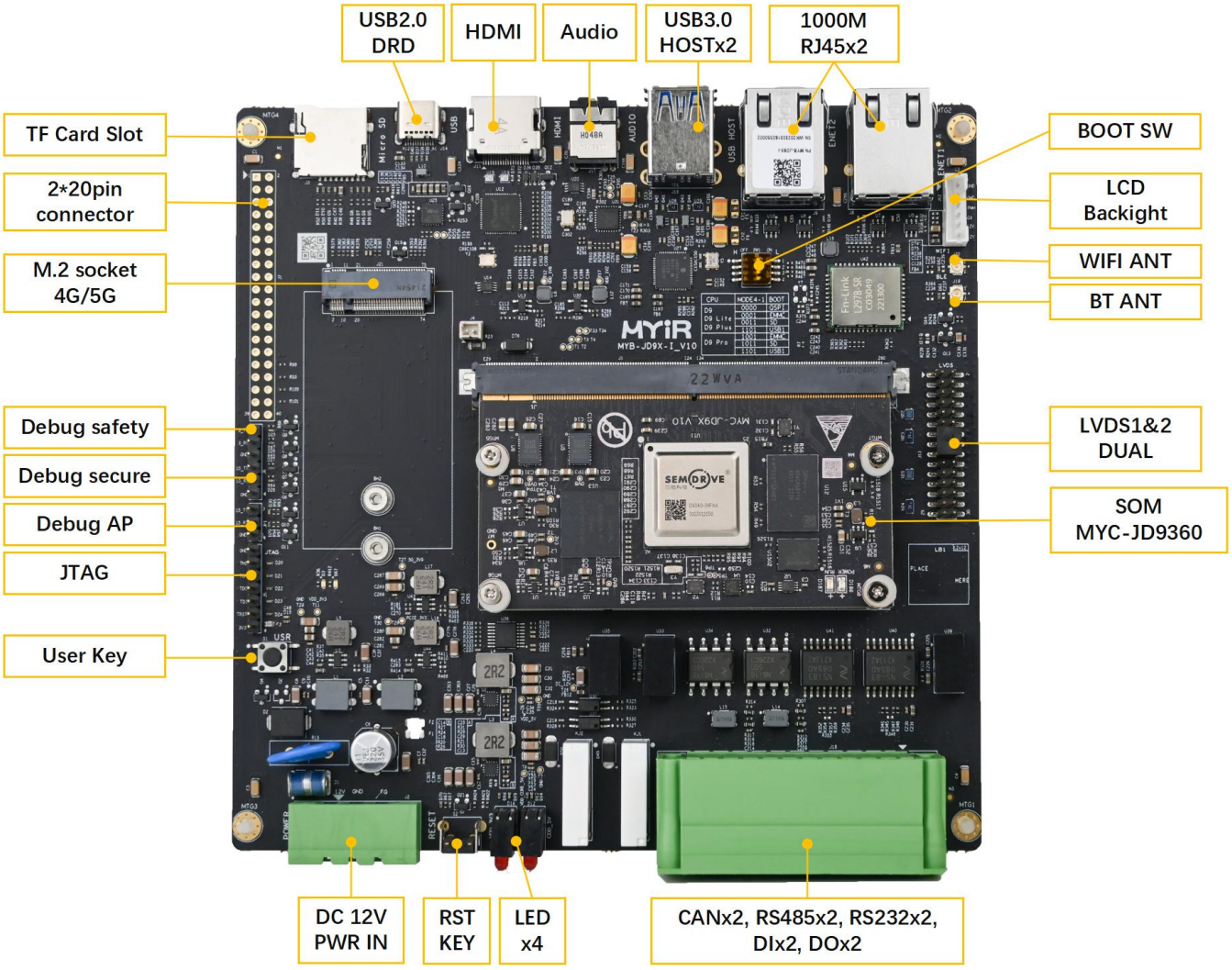
The **MYD-JD9360 Development Board** is built around the **MYC-JD9360 SOM** and has explored many features of the D9-Pro SoC through one 0.5mm pitch 314-pin MXM 3.0 gold-finger-edge-card connector. It has rich communication interfaces including two RS232, two RS485, two USB3.0 HOST and one USB2.0 OTG, two TSN enabled Gigabit Ethernet, two CAN, one Micro SD card slot, one SDIO/UART based WiFi/BT module, one M.2 Socket for USB3.0 based 4G/5G LTE Module with one SIM card holder and one M.2 Socket for PCIE3.0 based SSD module. It also has advanced multi-media capabilities to support HDMI display, dual LVDS display, audio and camera. The **MY-CAM003M MIPI Camera Module** and **MY-LVDS070C LCD Module** can be used as options for the MYD-JD9360 board which allows customers to acquire better development experience.



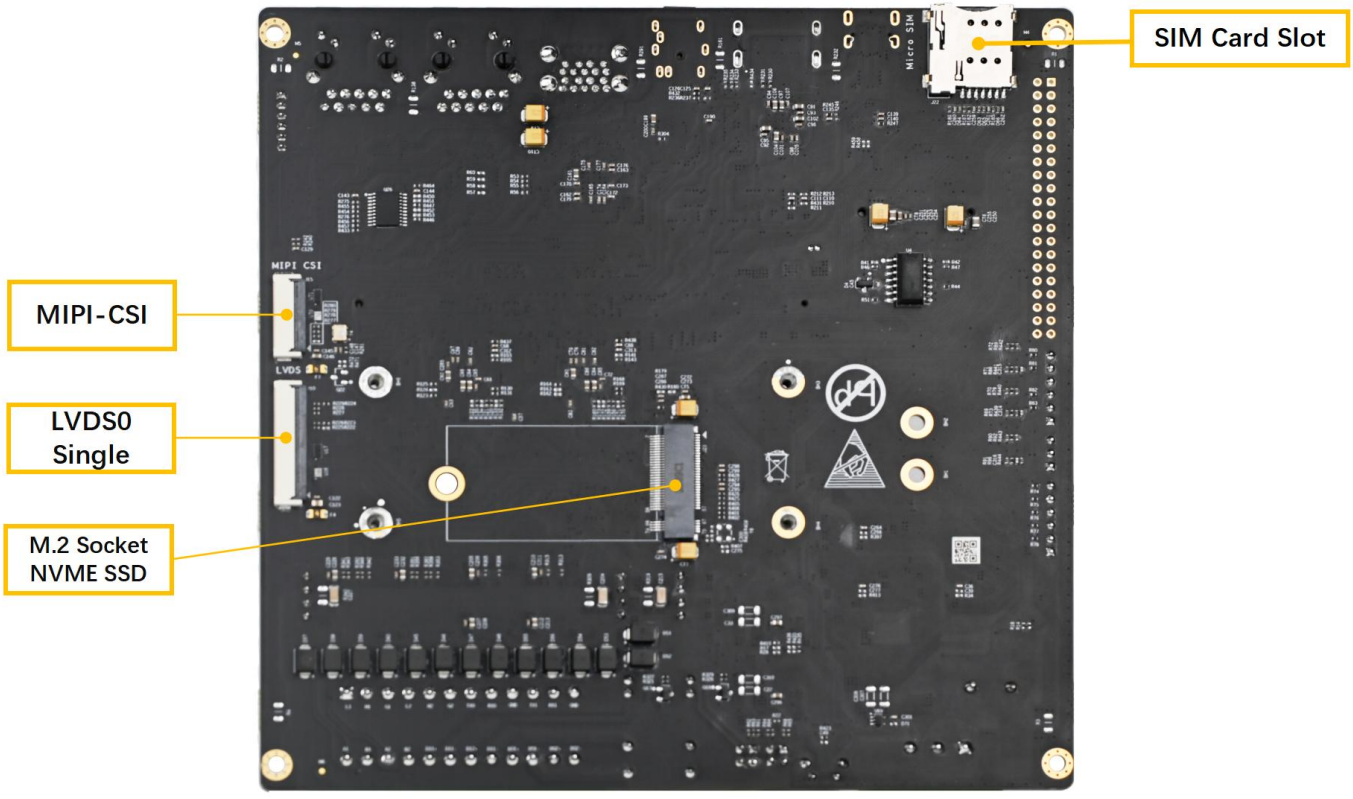
MYC-JD9360 SOM (Top-view)



MYC-JD9360 SOM (Bottom-view)

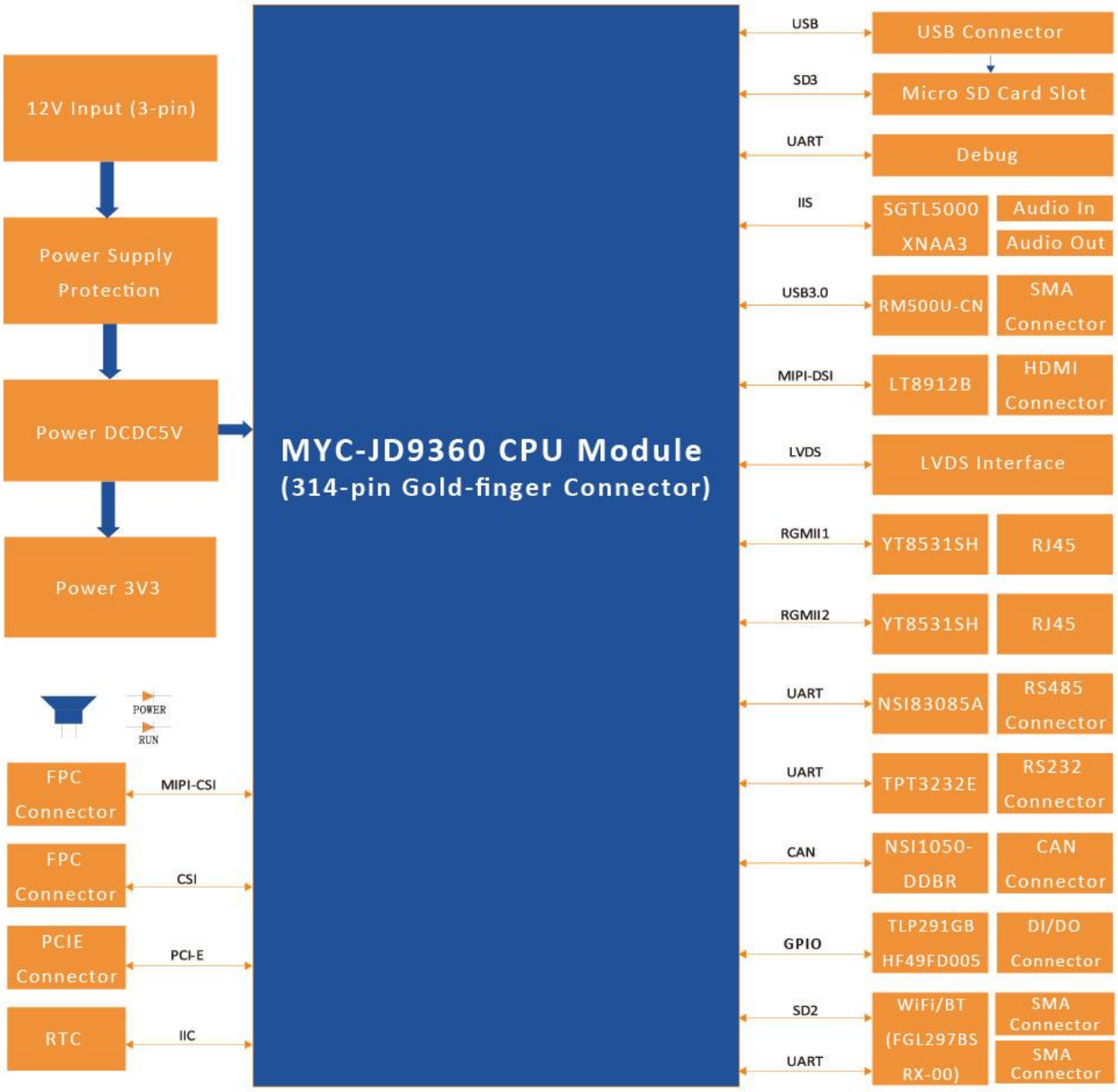


MYD-JD9360 Development Board (Top-view)



MYD-JD9360 Development Board (Bottom-view)

MYIR provides a wealth of software resources and documentation for Linux operating system support on the MYD-JD9360 Development Board to help developers start their development easily. Software resources include but are not limited to, Linux kernel, bootloader, drivers images and related development tools.



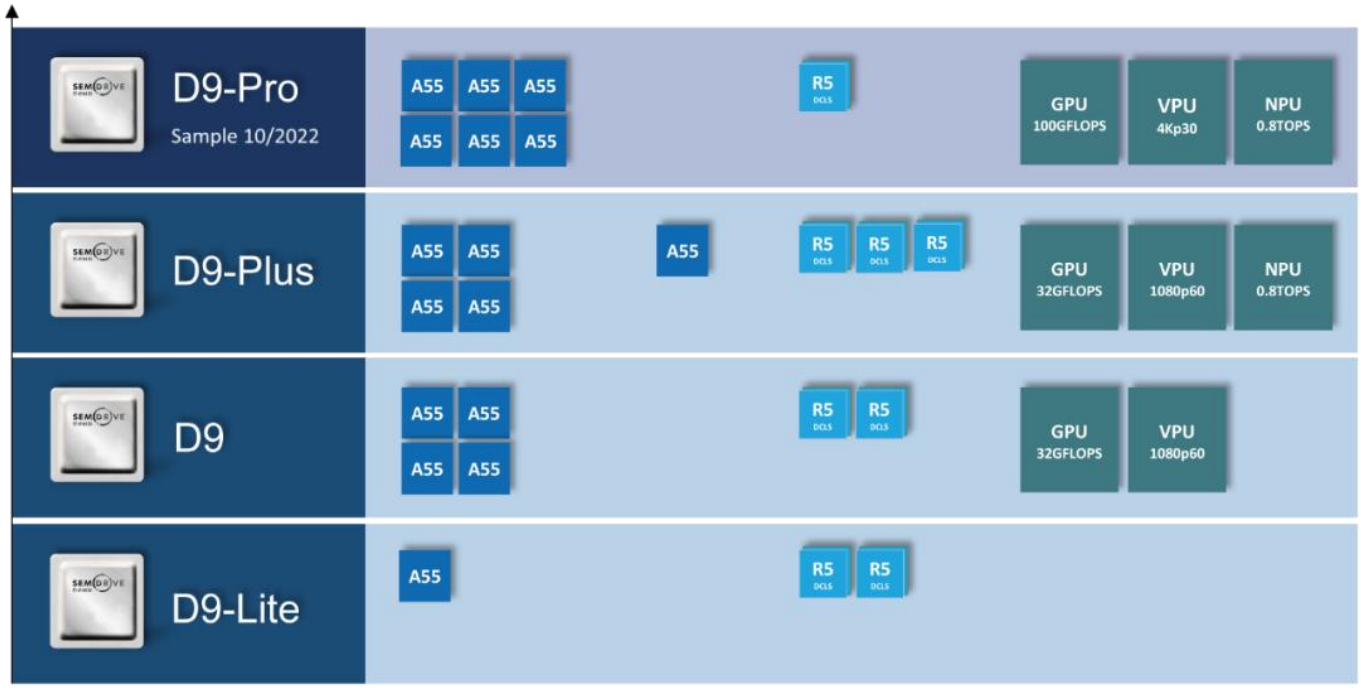
MYD-JD9360 Function Block Diagram

Hardware Specification

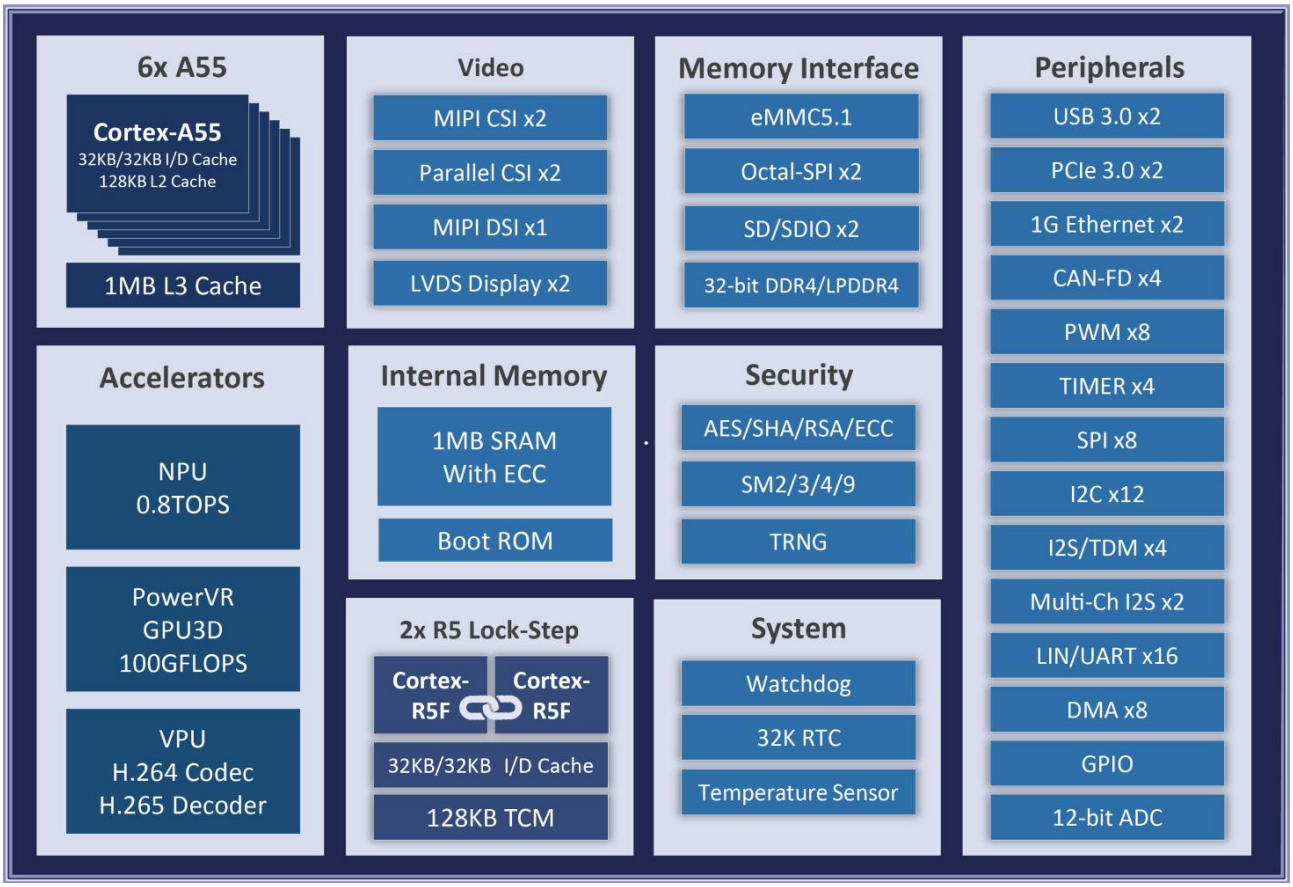
The **MYC-JD9360 SOM** on the **MYD-JD9360 Development Board** is using SemiDrive's D9-Pro (D9360) application chip which is among the high-reliability, high-security, high real-time and high-performance D series industrial chips. The D9-Pro integrates the high-performance ARM Cortex-A55 CPU and real-time ARM Cortex-R5 CPU. It includes 3D GPU, AI Accelerator, and H.264 video encoder/decoder. D9-Pro processor also supports various connectivity interfaces, including PCIe3.0, USB3.0, Giga-bit Ethernet, CAN-FD, UART, SPI for seamless integration into industry system at a minimal BOM cost. It is designed specially for motion control and various industrial applications.

The main features of D9-Pro chip are as follows:

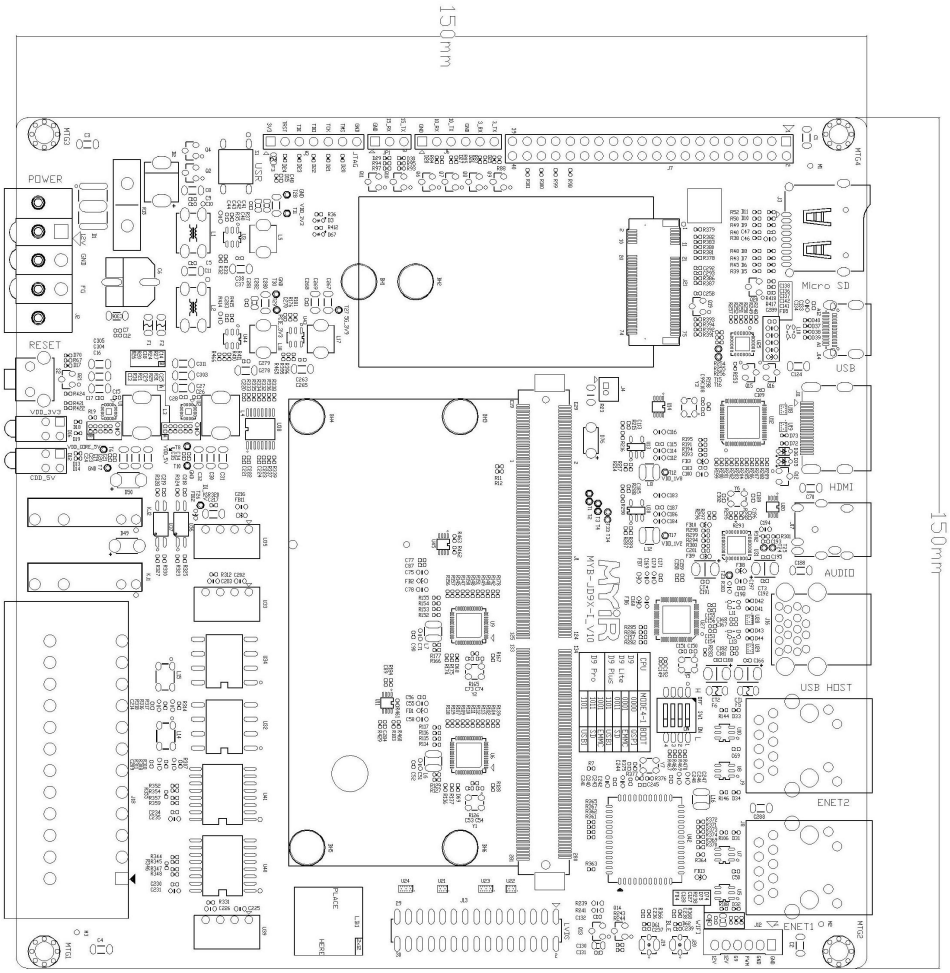
1) Low power consumption: TSMC's 16nm FinFET advanced process guarantees relatively low power consumption
2) High performance CPU: 45.2KDMIPS (6*Cortex-A55) +1.6KDMPIS (Cortex-R5)
3) Built-in large Cache, large bandwidth DDR4 2400MT/S
4) High performance 3D graphics processing: 100GFLOPS PowerVR 9XM GPU
5) High efficiency accelerator for AI: 0.8Tops NPU
6) HD vision processing unit (VPU): H.264 video encoding/decoding at 4Kp30, H.265 decoding at 4Kp30
7) Multi-screen display: support dual display at 1080p resolution of different contents; support the third HMI display through Cortex-R5 co-processor control



D9 Series Comparison



D9-Pro Block Diagram



MYD-JD9360 Dimensions Chart

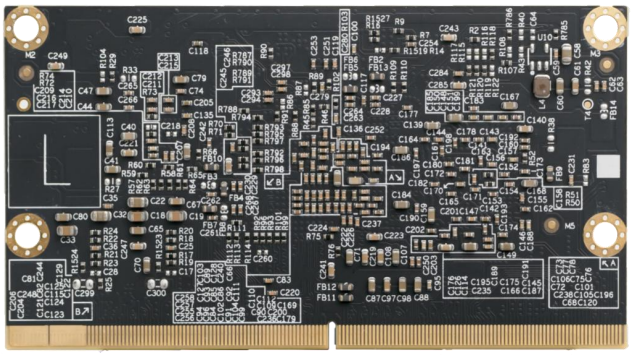
The MYD-JD9360 development board consists of a SOM MYC-JD9360 and a base board to expose many features of the SemiDrive D9-Pro processors through one 0.5mm pitch 314-pin MXM 3.0 gold-finger-edge-card connector. It takes full features of D9-Pro processor and the main features are characterized as below:

Mechanical Parameters

- Dimensions: 150mm x 150mm (base board), 82mm x 45mm (SOM)
- PCB Layers: 6-layer design (base board), 8-layer design (SOM)
- Power supply: +12V/2A (base board), +5V/5A (SOM)
- Working temperature: -40~85 Celsius (industrial grade)

The MYD-JD9360 Controller Board (MYC-JD9360 SOM)

MYC-JD9360 SOM (Top-view)



MYC-JD9360 SOM (Bottom-view)

Processor

- SemiDriver D9-Pro processor
  - 6 * Cortex-A55 @ 1.6GHz
  - 1 * Cortex-R5 @ 800MHz, dual-core Lock-step
  - IMG PowerVR 9XM GPU, 100GFLOPS
  - VPU H.264 Video Encoder/Decoder, H.265/VP8/VP9 Video Decoder
  - 0.8Tops NPU (SemiDrive SlimAI Engine)

Memory

- 2GB LPDDR4 (supports up to 4GB)
- 16GB eMMC (supports up to 128GB)
- 256Kbit EEPROM
- 16MB QSPI Nor Flash

Peripherals and Signals Routed to Pins

- 0.5mm pitch 314-pin MXM 3.0 Gold-finger-edge-card Connector
  - 2 x Gigabit Ethernet
  - 2 x PCIe3.0
  - 2 x USB3.0 (DRD)
  - 2 x SDIO
  - 11 x UART (supports up to 16 x UART)
  - 2 x CAN-FD (supports up to 4 x CAN-FD)
  - 4 x I2C (supports up to 12 x I2C)
  - 2 x SPI (supports up to 8 x SPI)

- 4 x 12bit ADC
- 1 x MIPI-DSI
- 2 x LVDS
- 1 x Parallel CSI
- 1 x MIPI CSI
- 4 x Single-channel I2S/TDM
- 2 x Multi-channel I2S
- 1 x JTAG
- Up to 135 x GPIOs

Note: the peripheral signals brought out to the expansion interface are listed in maximum number. Some signals are reused. Please refer to the processor datasheet and the SOM pinout description file.

The MYD-JD9360 Development Board Base Board

- Power Input (3-pin Phoenix terminal)
- Serial Ports
  - 2 x RS485 interface (with isolation)
  - 2 x RS232
  - 1 x JTAG interface
  - 1 x Debug AP port (Cortex-A55)
  - 1 x Debug Secure port (Cortex-R5)
- USB
  - 2 x USB3.0 Host ports
  - 1 x USB2.0 DRD port
  - 1 x M.2 socket for USB based 4G/5G LTE Module
- 1 x SIM card slot
- 1 x WiFi/BT Module
- 2 x 10/100/1000Mbps Ethernet interfaces
- 2 x CAN FD interface (with isolation)
- 1 x Micro SD card slot
- 1 x Dual-channel LVDS Display Interface
- 1 x Single-channel LVDS Display Interfaces
- 1 x HDMI Display Interface
- 1 x MIPI-CSI Camera Interface
- 1 x Audio Input and Output Interface
- 1 x 2.54mm 2 x 20-pin male expansion header (ADC/OSPI/SPI interface)
- 2 x Keys (one for RESET and one for USER)
- 4 x LED
  - 1 x System indicator
  - 1 x 5G/4G module status LED
  - 1 x Power LED
  - 1 x User LED
- 2 x Digital Input (DI) interfaces (with isolation)
- 2 x Digital Output (DO) interfaces (with isolation)

Software Features

The **MYD-JD9360 Development Board** support for Linux, Ubuntu and Android operations systems. The kernel and many peripheral drivers are available in source code to assist clients to expedite their development. The following are a summary of the software features:

Item	Feature	Description	Source code
Bootloader	U-boot	Boot Program uboot_2021.04	YES
Linux kernel	Linux kernel	Based on the official kernel_4.14.61 version customization	YES
Device driver	QSPI	W25Q128JVEIQ driver	YES
	Watchdog	SGM820B driver	YES
	USB Host	USB Host driver	YES
	USB OTG	USB OTG driver	YES
	I2C	I2C bus driver	YES
	SPI	SPI bus driver	YES
	Ethernet	YT8531SH driver	YES
	SDHI	eMMC/SD card storage driver	YES
	HDMI	LT8912 driver	YES
	LVDS	LT8912 driver	YES
	Dual LVDS	Dual LVDS driver	YES
	Audio	SGTL5000 audio driver	YES
	4G/5G	4G/5G driver	YES
	PWM	PWM control	YES
	ADC	ADC driver	YES
	RTC	RTC driver	YES
	GPIO	Universal GPIO driver	YES
	UART	RS232/RS485/TTL driver	YES
	CAN	CAN driver	YES
	Camera (MIPI)	OV5640 camera driver	YES
	WiFi	L297B-SR driver	YES
File system	myir-image-full	Full function image built with Yocto	YES
Industry DEMO	Application of Charging pile	Refer to the State grid charging pile program to realize the meter Modbus protocol, IEC104 platform communication protocol and charging demonstration interface. Integrate into the MeasyHMI V2.0 version and demonstrate through full image.	YES
	PLC controller	Port the open source Ethercat master IGH, Linux real-time patch PREEMPT-RT or XENOMAI (real-time response speed and real-time jitter time measured data), and write a console application to control EtherCAT slave and servo motor by command.	YES
	Engineering Machinery Scenarios	Four AHD cameras capture four images and display them on the screen. The Analog instrument information is displayed on the screen, and the video picture and instrument information are displayed on split screens. Integrate into the MeasyHMI V2.0 version and demonstrate through full image.	YES

MYD-JD9360 Software Features

Order Information

Product Item	Part No.	Packing List
MYD-JD9360 Development Board	MYD-JD9360-16E2D-160-I	✓ One MYD-JD9360 Development Board ✓ One USB to TTL cable ✓ One 12V/2A Power adapter ✓ One DC Power jack adapter ✓ One Quick Start Guide
MYC-JD9360 System-On-Module	MYC-JD9360-16E2D-160-I-G	✓ One MYC-JD9360 SOM
MY-CAM003M Camera Module	MY-CAM003M	**Add-on Options** ✓ MY-LVDS070C 7-inch LCD Module ✓ MY-CAM003M MIPI Camera Module
MY-LVDS070C LCD Module	MY-LVDS070C	

Note:

1. One MYD-JD9360 Development Board includes one SOM MYC-JD9360 mounted on the base board. If you need more SOM, you can order extra ones.

2. Discounts are available for bulk orders.

3. The MYC-JD9360 SOM and the MYD-JD9360 development board are delivered with heatsink by default.

4. We provide OEM/ODM services to reduce time and save cost for customers.

MYIR Electronics Limited
Headquarter Address: Room 04, 6th Floor, Building No.2, Fada Road, Yunli Smart Park, Bantian, Longgang District, Shenzhen, Guangdong, China 518129

Factory Address: Room 201, Block C, Shengjianli Industrial Park, Dafu Industrial Zone, Guanlan, Longhua District, Shenzhen, 518110, China

Website: en.myir.cn
Email: sales@myir.cn
Tel: +86-755-22984836